

Schematics Version History Table :

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1.0	1.0	32	uATX 244mmx220mm Without PCI3	02/23/' 06

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DEVICE	IDSEL	INT#	REQ#	GNT#
PCI1	16	C/D/E/F	PREQ-1	PGNT-1
PCI2	17	D/E/F/G	PREQ-2	PGNT-2
PCI3	18	E/F/G/H	PREQ-3	PGNT-3
LAN	25	F	PREQ-4	PGNT-4

Clock Gen
ICS952647
48pin SSOP

intel
P4 Processor
775 pin

re.2.0 Prescott, Cedar Mill & Smithfield

BW : 4.1GB/s @ FSB : 533MHz & Freq : 133MHz
BW : 6.4GB/s @ FSB : 800MHz & Freq : 200MHz

BW : 1.066GB/s @ Freq : 66MHz
AGP1 Slot 132p
8X / 1.5V
VGA (G only)

intel
iSP/G
932pin FC-BGA

BW : 2.1GB/s @ DDR : 200/266/333MHz & Freq : 133MHz
BW : 3.2GB/s @ DDR : 266/333/400MHz & Freq : 200MHz

DDIMM1: DDR Socket 184P
DDIMM2 : DDR Socket 184P

BW : 266MB/s @ Freq : 66MHz

USB1 2 ports
USB2 2 ports
USB3 2 ports
USB4 2 ports
USBLAN 8 ports
IDE2 40pin
IDE1 40pin

USB V2.0

Up to Ultra ATA/100
Two IDE Channel

intel
ICH5
460pin EBGA

BW : 133MB/s @ Freq : 33MHz

PCI1 Slot 120pin @ AD16
PCI2 Slot 120pin @ AD17

SATA1 7Pin
SATA2 7pin

AC' 97 & Lan I/F

Audio Codec
ALC655

intel
FWH
32pin PLCC


LPC bus

Lan Chip
RTL8100C
100pin

USBLAN
RJ45

Mic In
Line Out
Line In

Super I/O
W83627EHF
128pin PQFP

Elitegroup Computer Systems

Title

System Block Diagram

Size

Document Number

Rev

1.0

1.0

Date:

Wednesday, March 01, 2006

Sheet

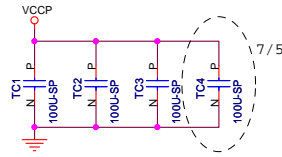
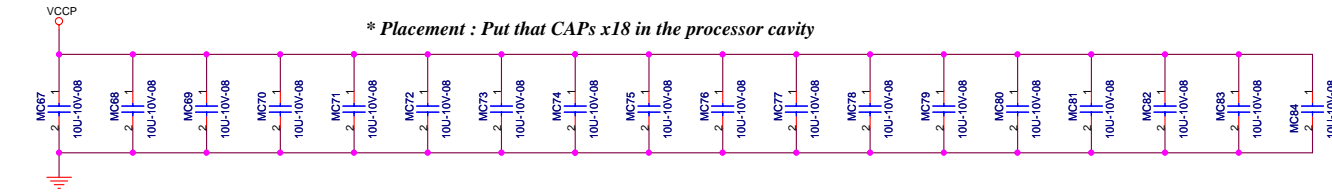
2

of

31

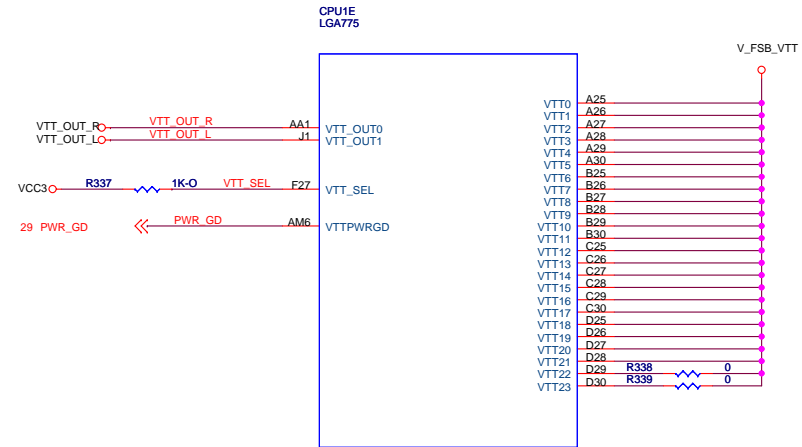
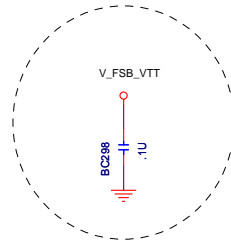


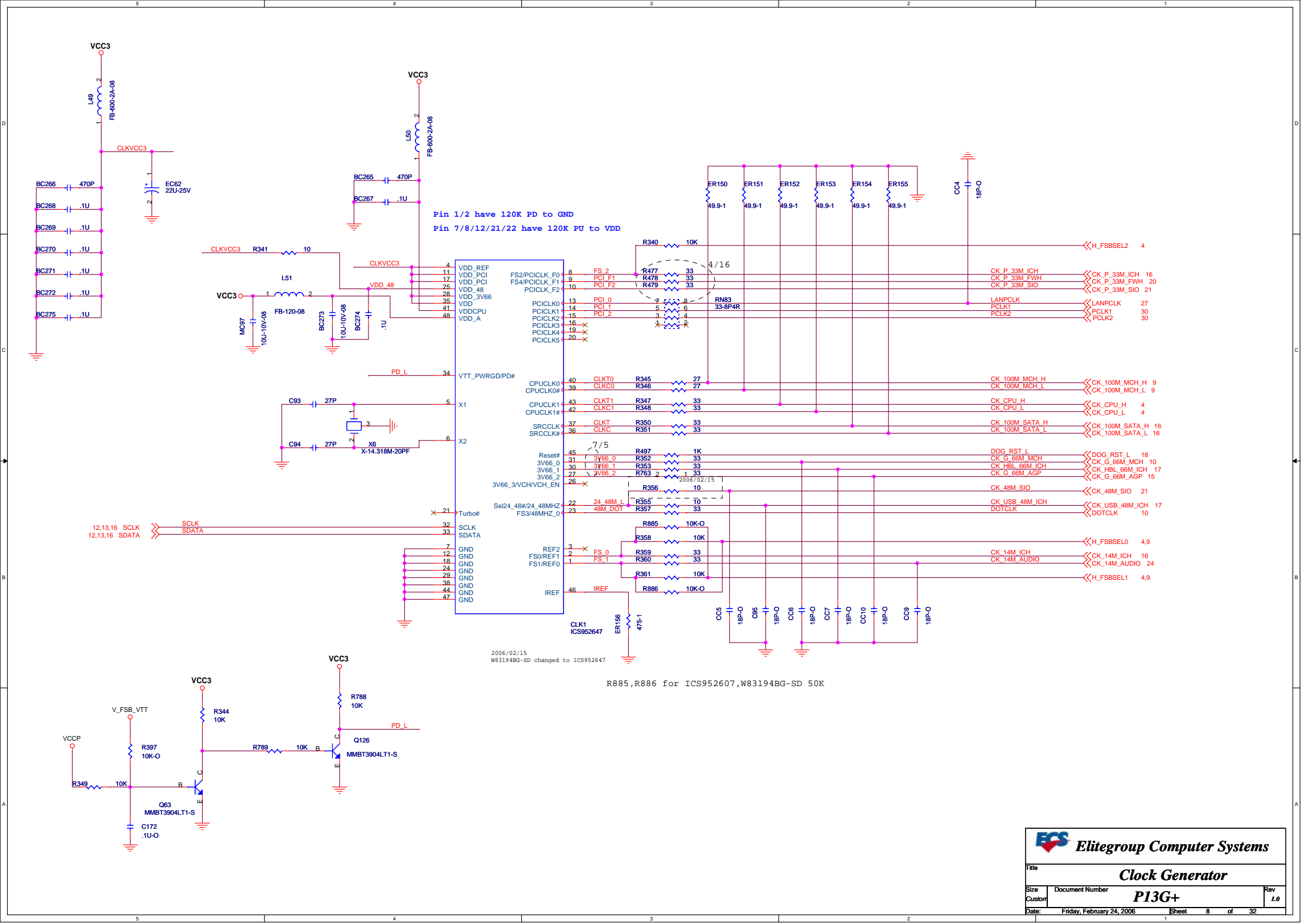
CPU1C
LGA775

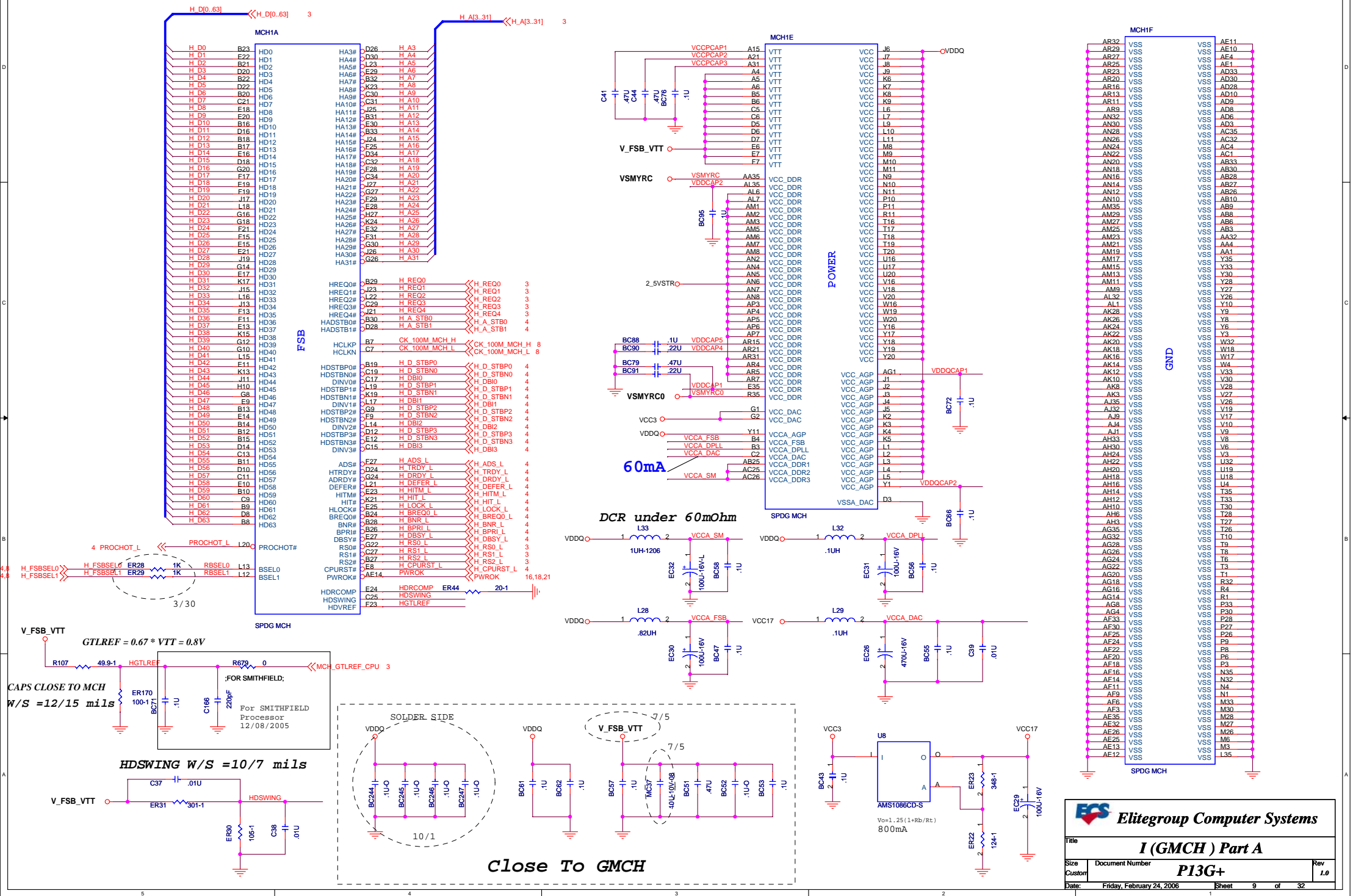


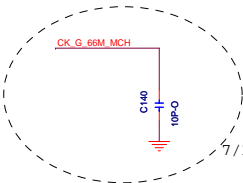
MC109,MC110 near to pin V26 & U27

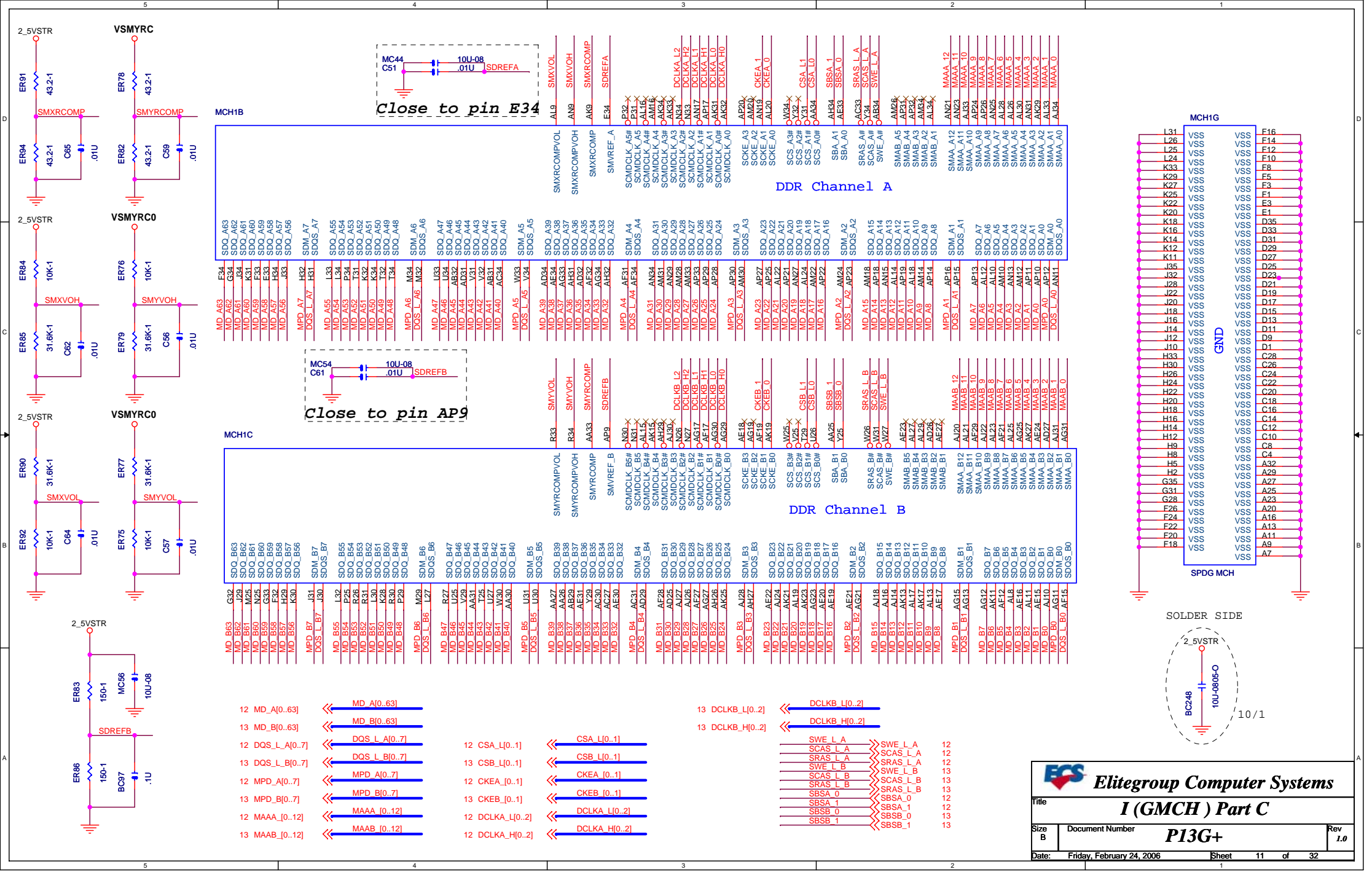
VTT_SEL=0 for the Tejas processor

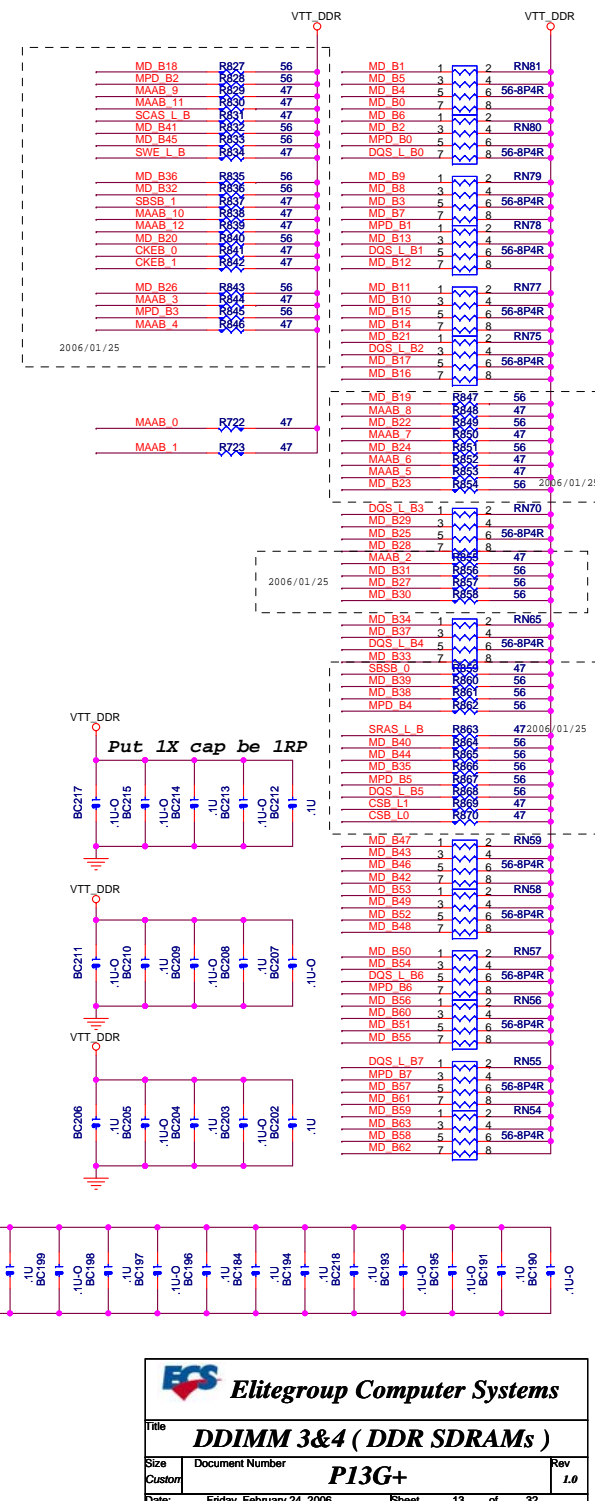


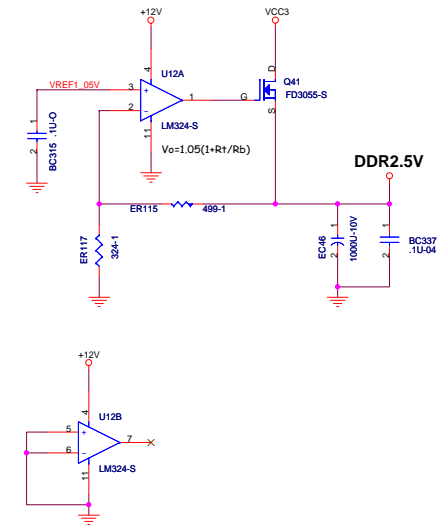
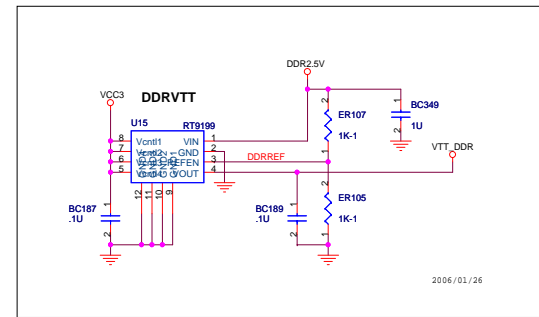
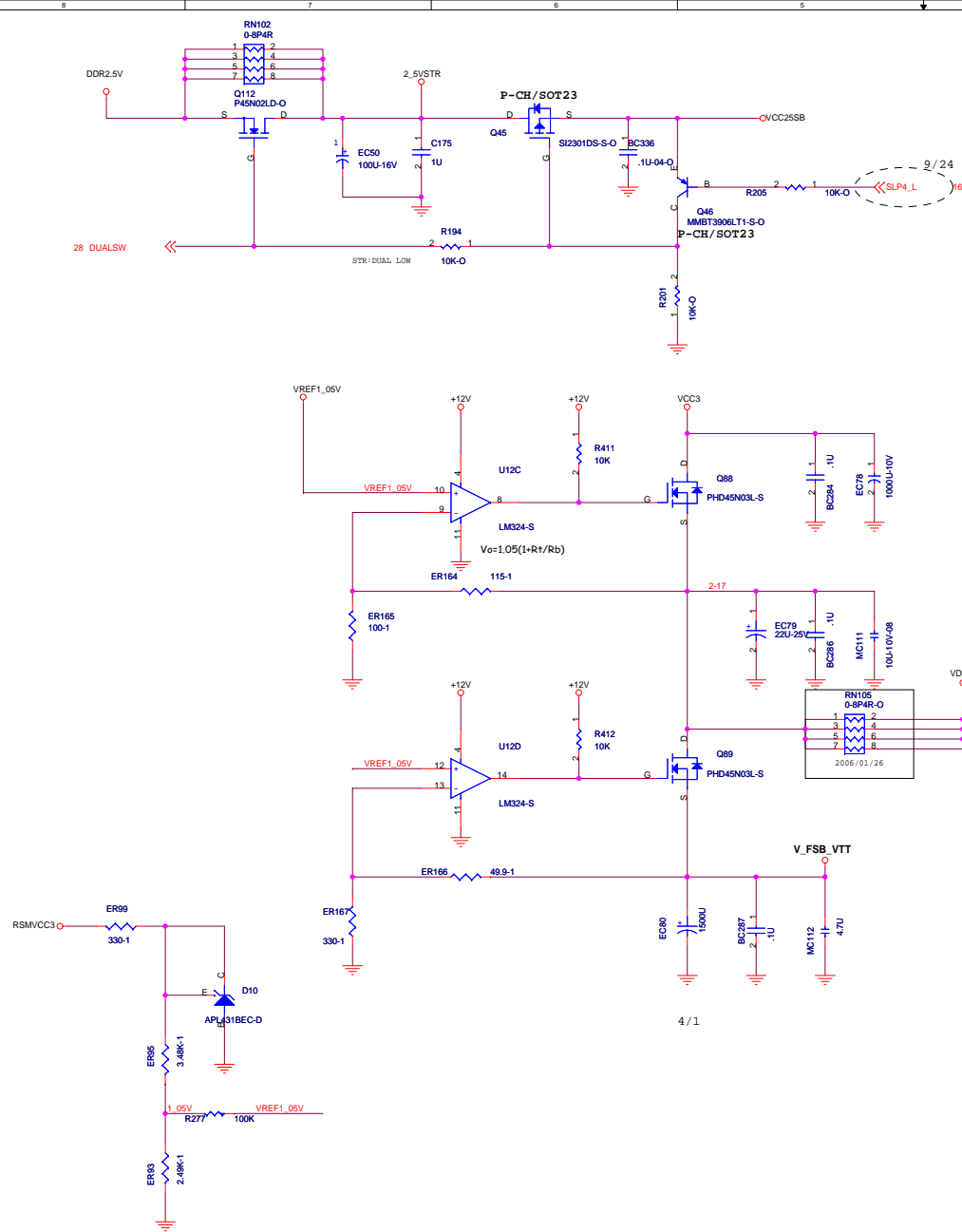












10 G_AD[0..31] << G_AD[0..31]

10 G_SBA[0..7] << G_SBA[0..7]

10 G_C_BE0 << G_C_BE0

10 G_C_BE1 << G_C_BE1

10 G_C_BE2 << G_C_BE2

10 G_C_BE3 << G_C_BE3

10 G_FRAME_L << G_FRAME_L

10 G_DEVSEL_L << G_DEVSEL_L

10 G_IRDY_L << G_IRDY_L

10 G_TRDY_L << G_TRDY_L

10 G_STOP_L << G_STOP_L

10 G_PAR << G_PAR

10 G_REQ_L << G_REQ_L

10 G_GNT_L << G_GNT_L

10 G_PIPE_L << G_PIPE_L

10 G_ADSTB0_H << G_ADSTB0_H

10 G_ADSTB0_L << G_ADSTB0_L

10 G_ADSTB1_H << G_ADSTB1_H

10 G_ADSTB1_L << G_ADSTB1_L

10 G_SBSTB_H << G_SBSTB_H

10 G_SBSTB_L << G_SBSTB_L

G_SERR_R_L R133 6.8K VDDQ

10 G_ST0 << G_ST0

10 G_ST1 << G_ST1

10 G_ST2 << G_ST2

10 G_DBILO << G_DBILO

10 G_RBF_L << G_RBF_L

10 G_WBF_L << G_WBF_L

8 CK_G_66M_AGP << CK_G_66M_AGP

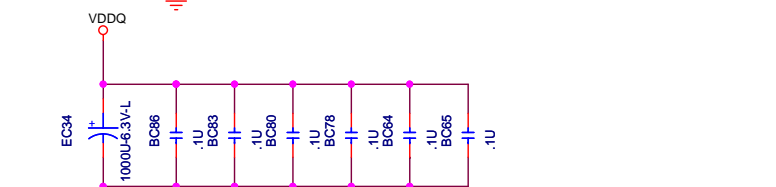
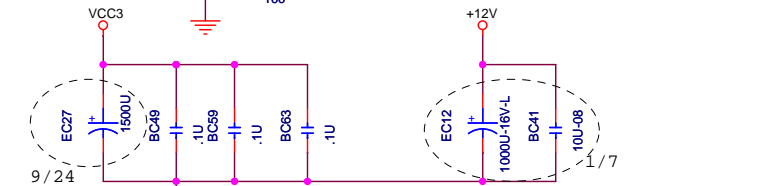
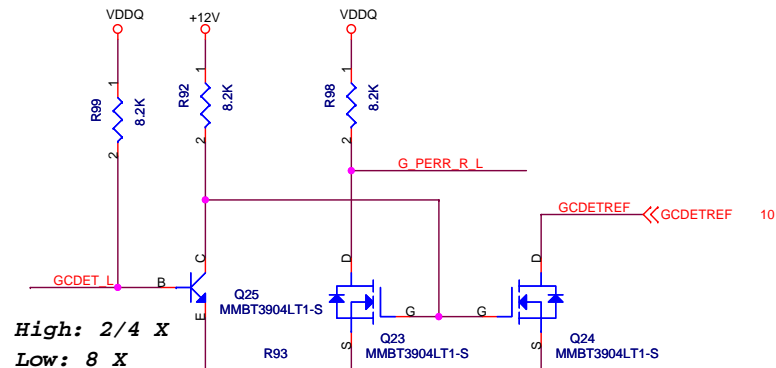
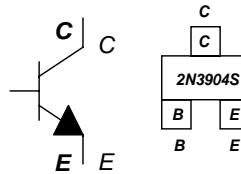
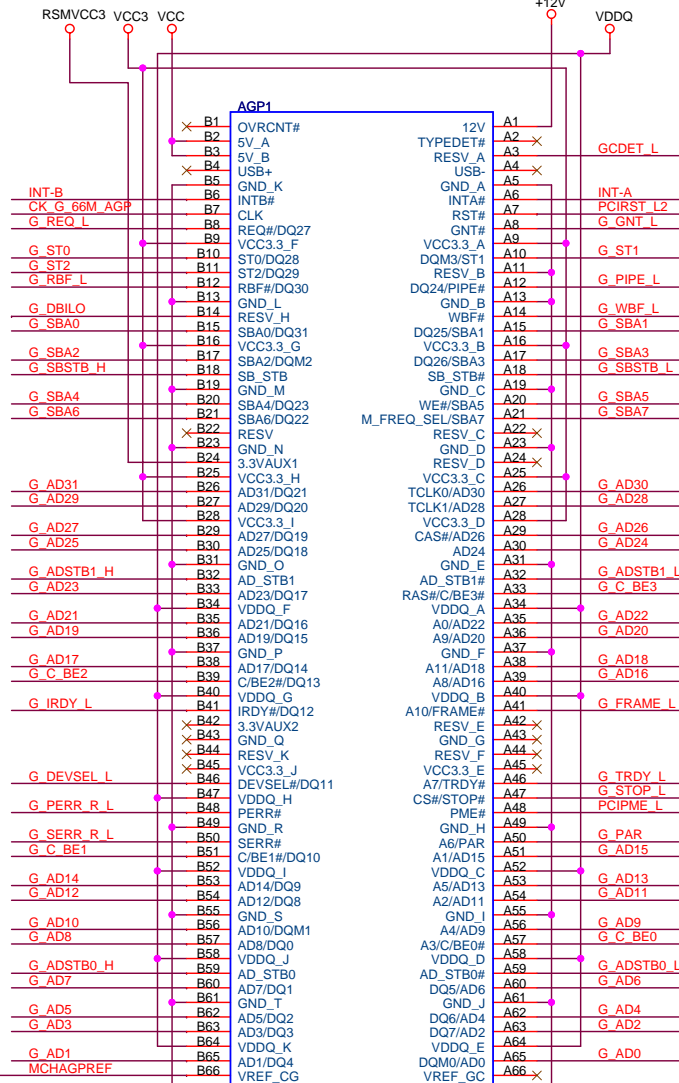
16,30 INT-A << INT-A

16,30 INT-B << INT-B

20,21,30 PCIRST_L2 << PCIRST_L2

16,27,30 PCIPME_L << PCIPME_L

10 MCHAGPREF << MCHAGPREF



Elitegroup Computer Systems

Title: **AGP Express**

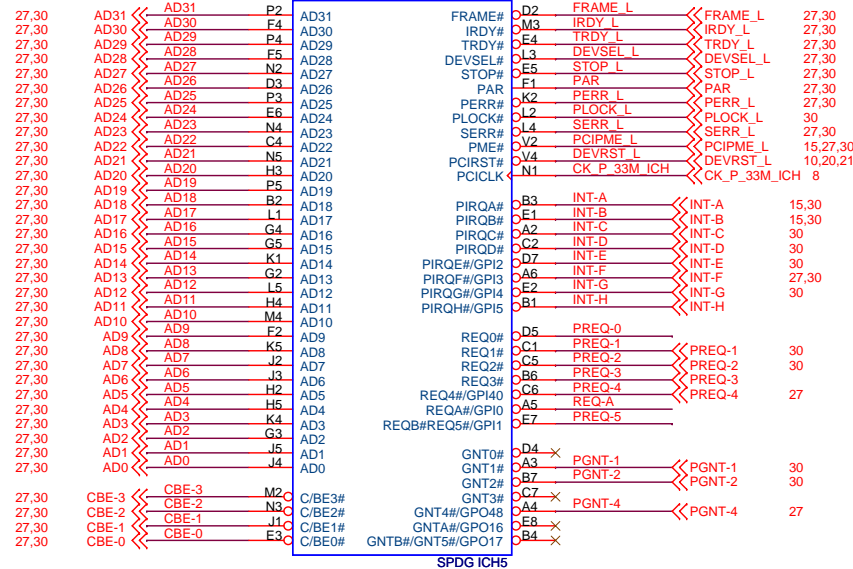
Size B: Document Number: **P13G+** Rev 1.0

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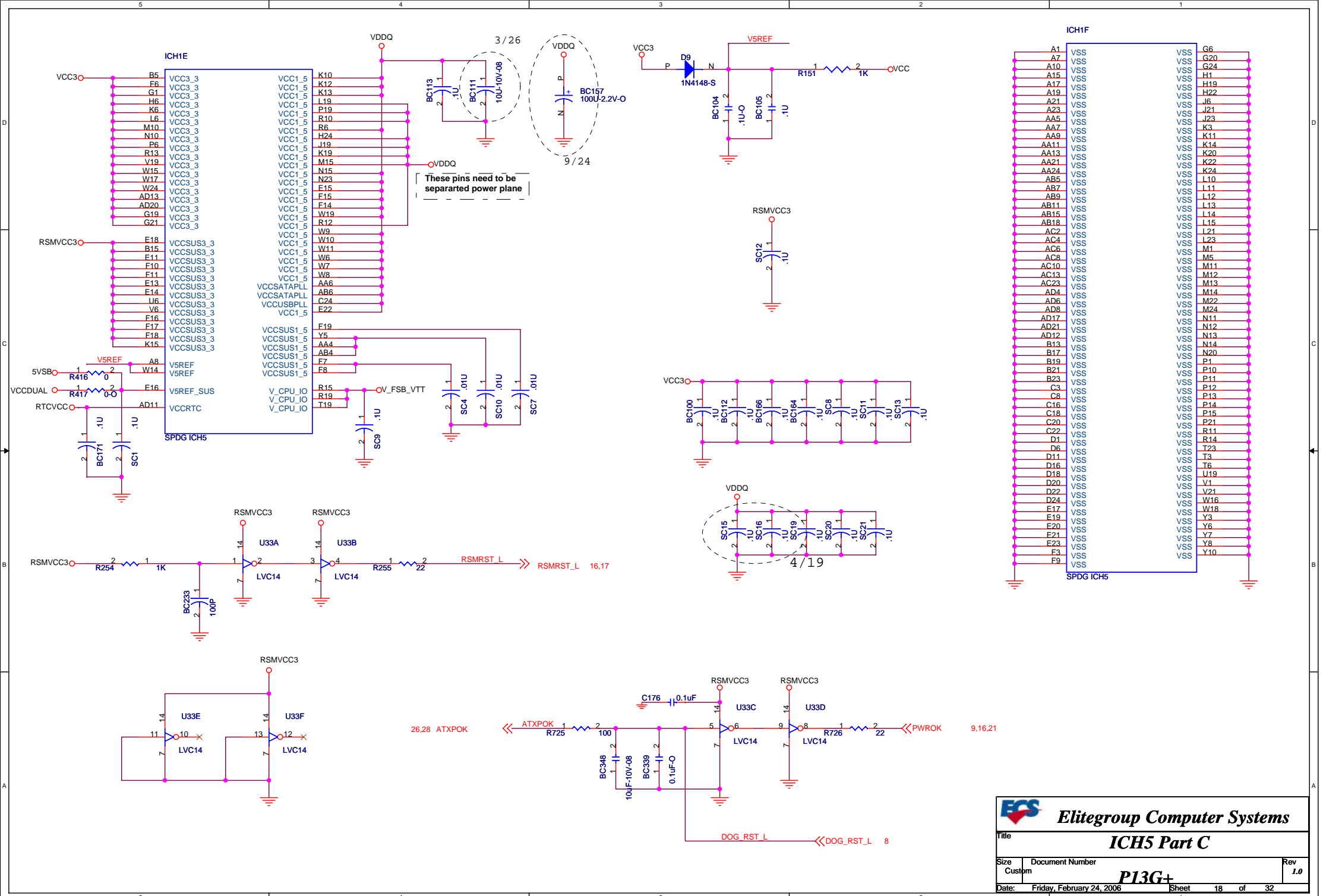
27,30 AD[31..0]

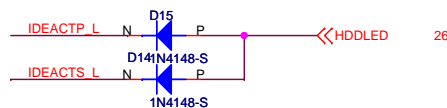
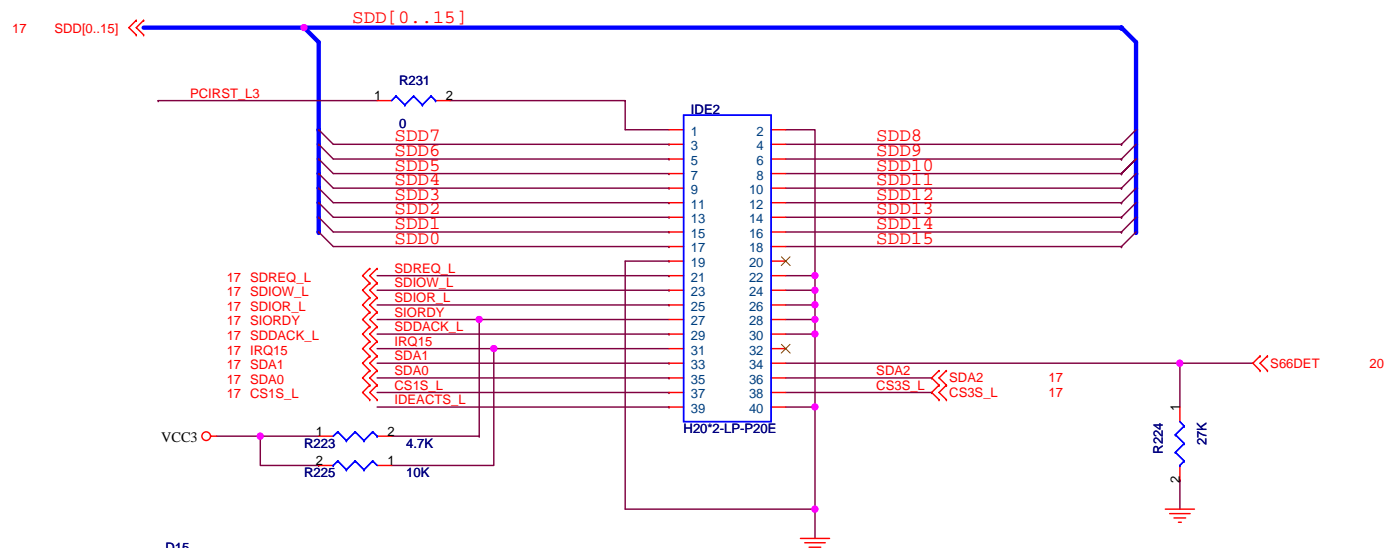
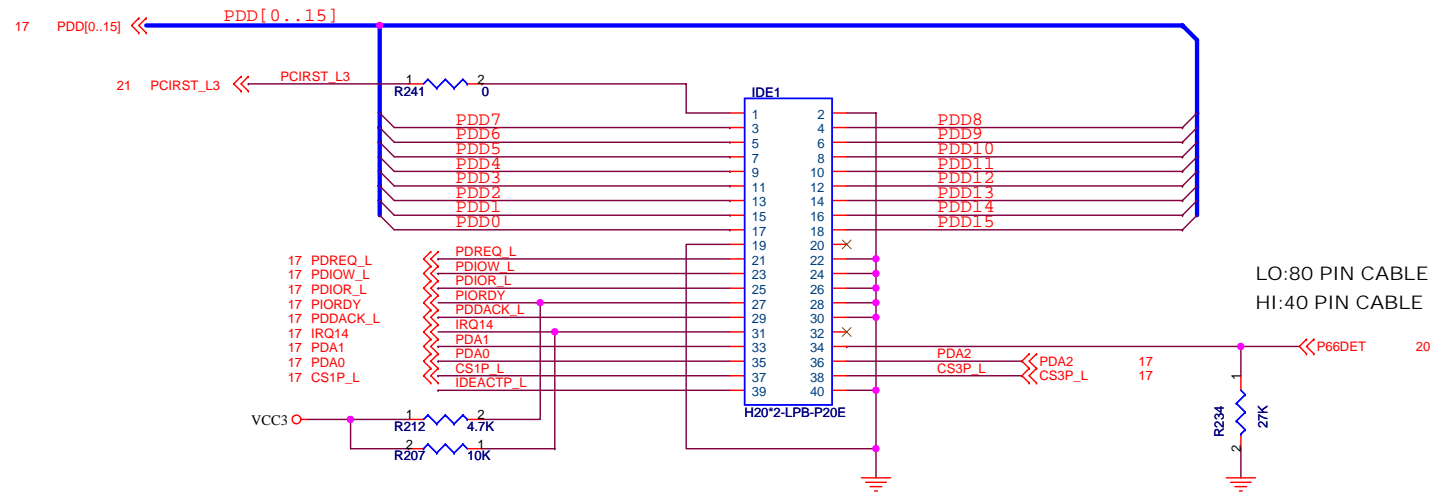
AD[31..0]

ICH1A



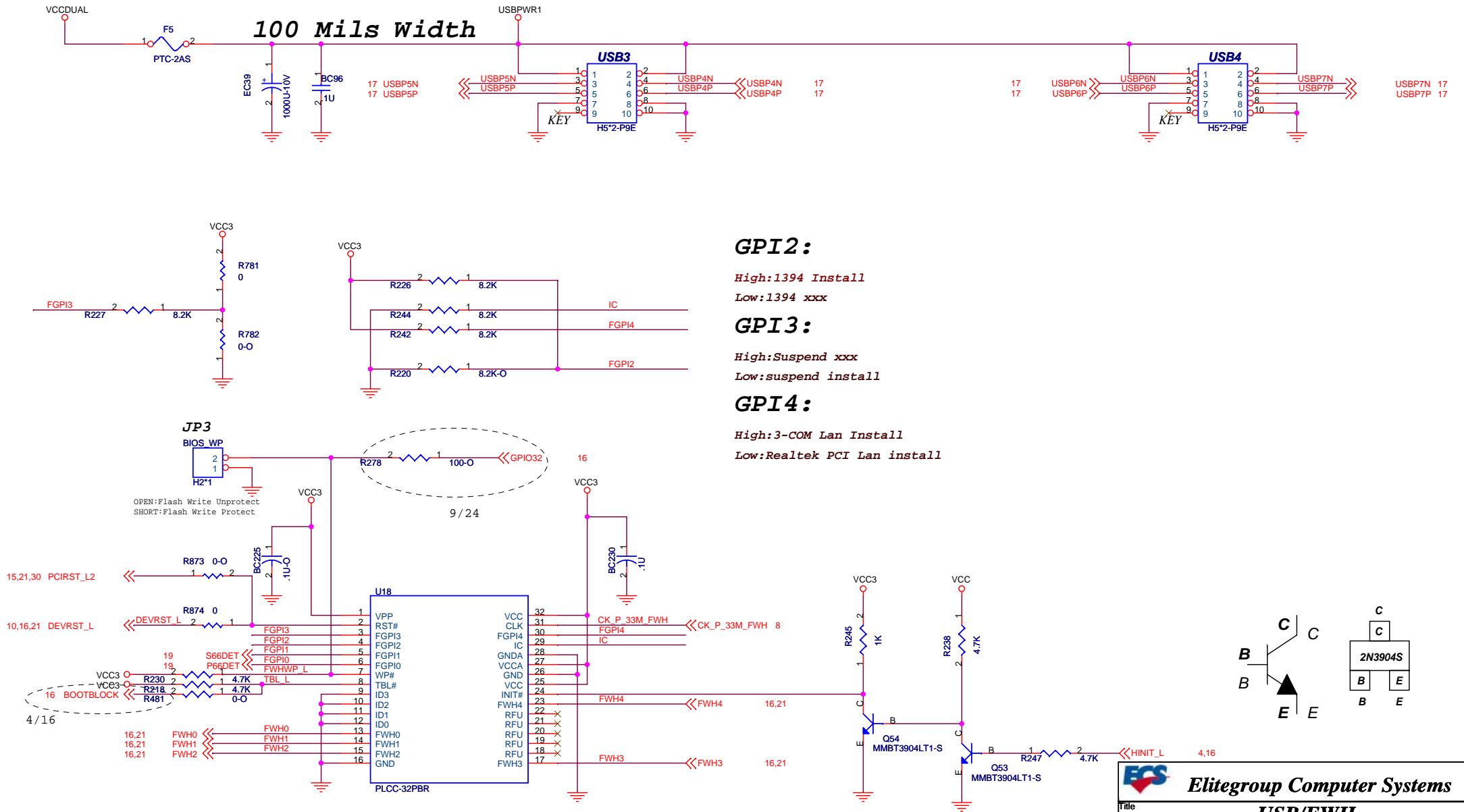






MAX TRACE LENGTH IS 8"

USB PORT INTERFACE



GPI2:

High:1394 Install

Low:1394 xxx

GPI3:

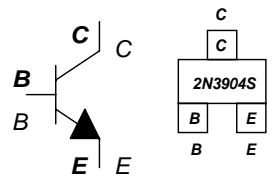
High:Suspend xxx

Low:suspend install

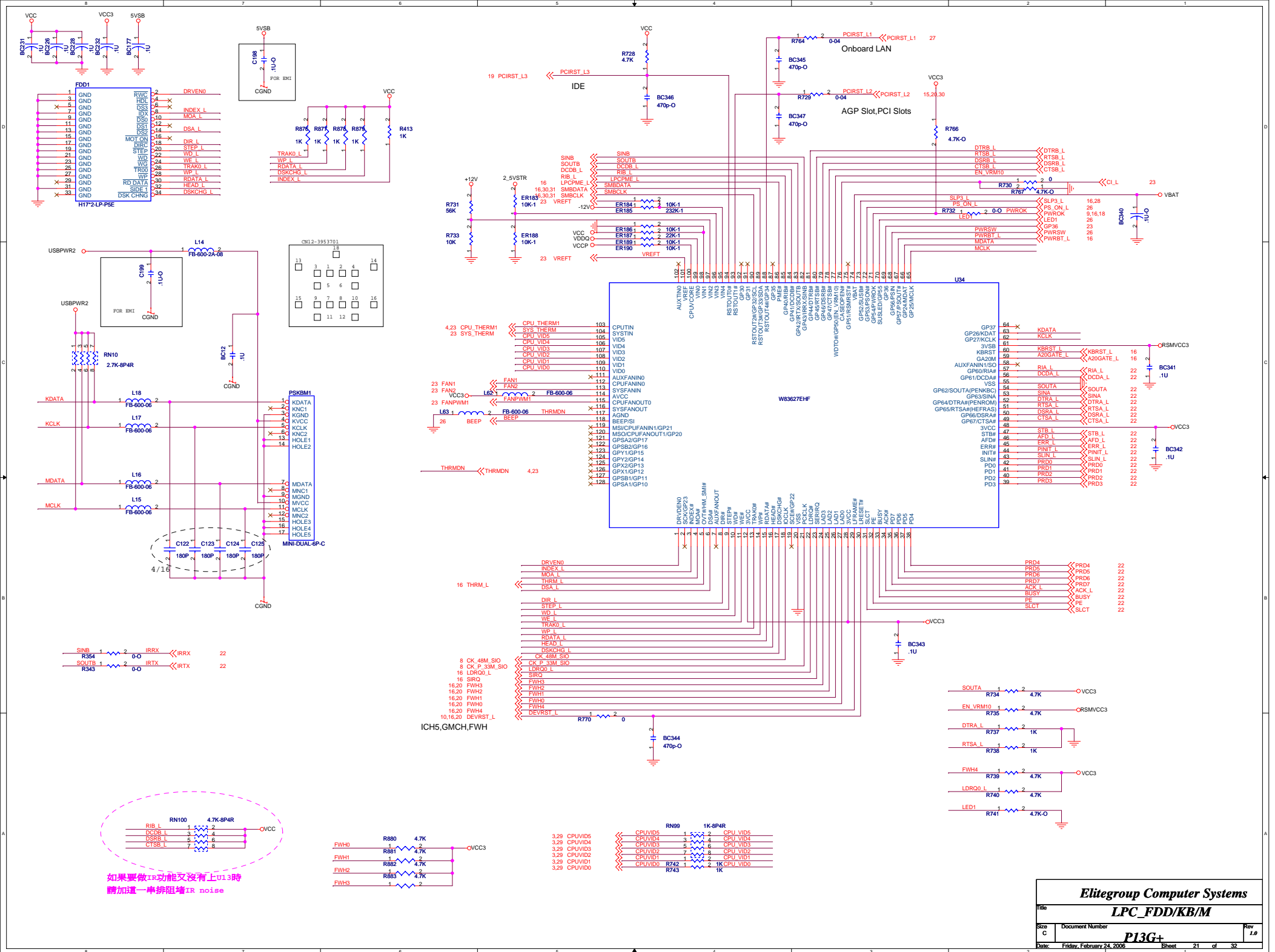
GPI4:

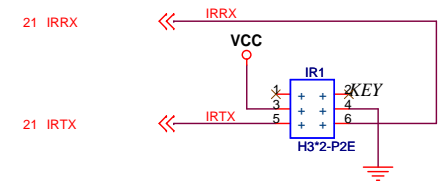
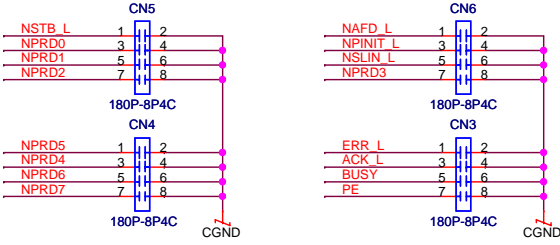
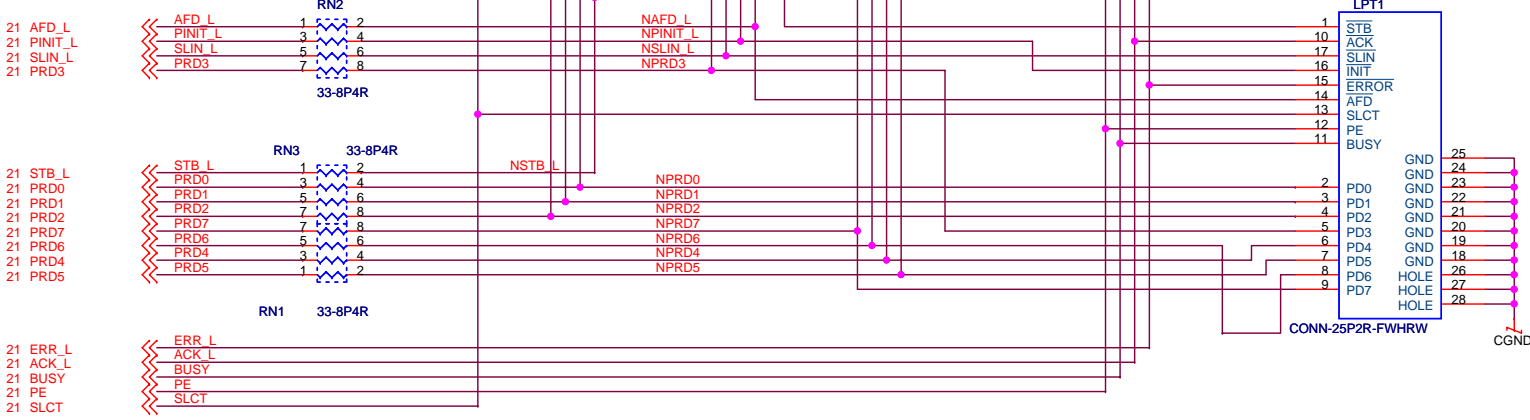
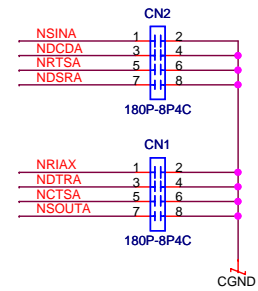
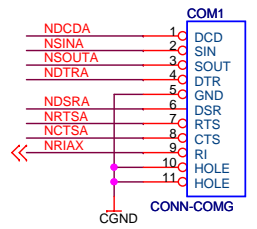
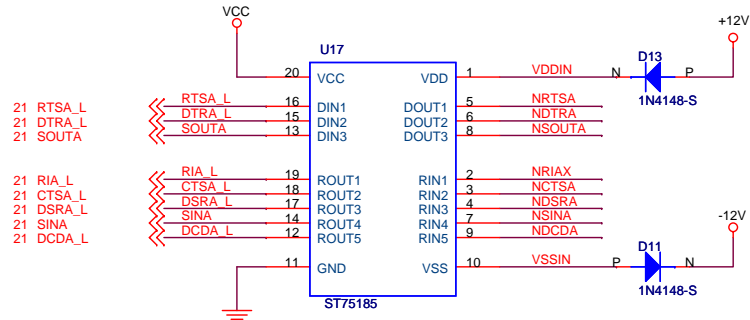
High:3-COM Lan Install

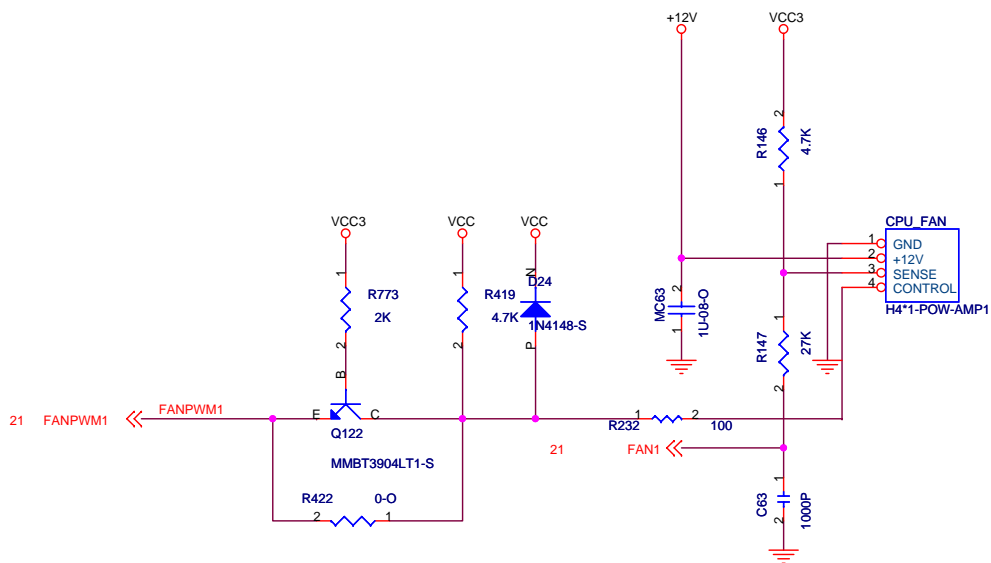
Low:Realtek PCI Lan install



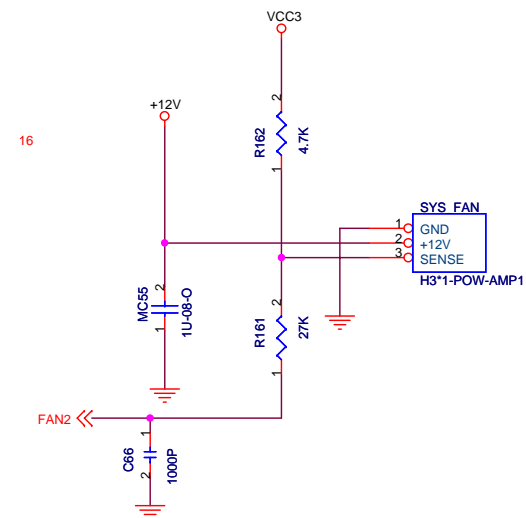
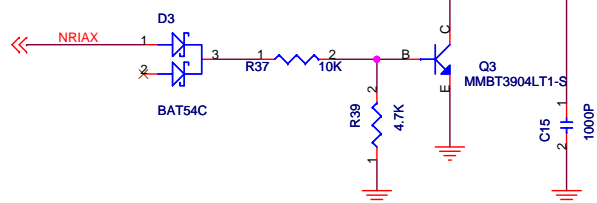
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USB/FWH		
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	Custom	P13G+
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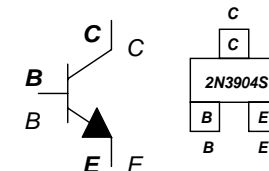
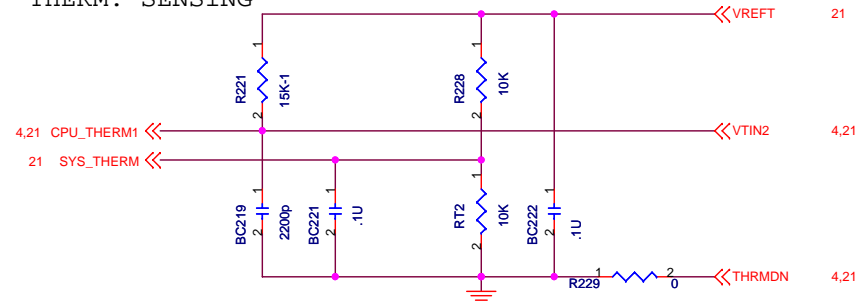


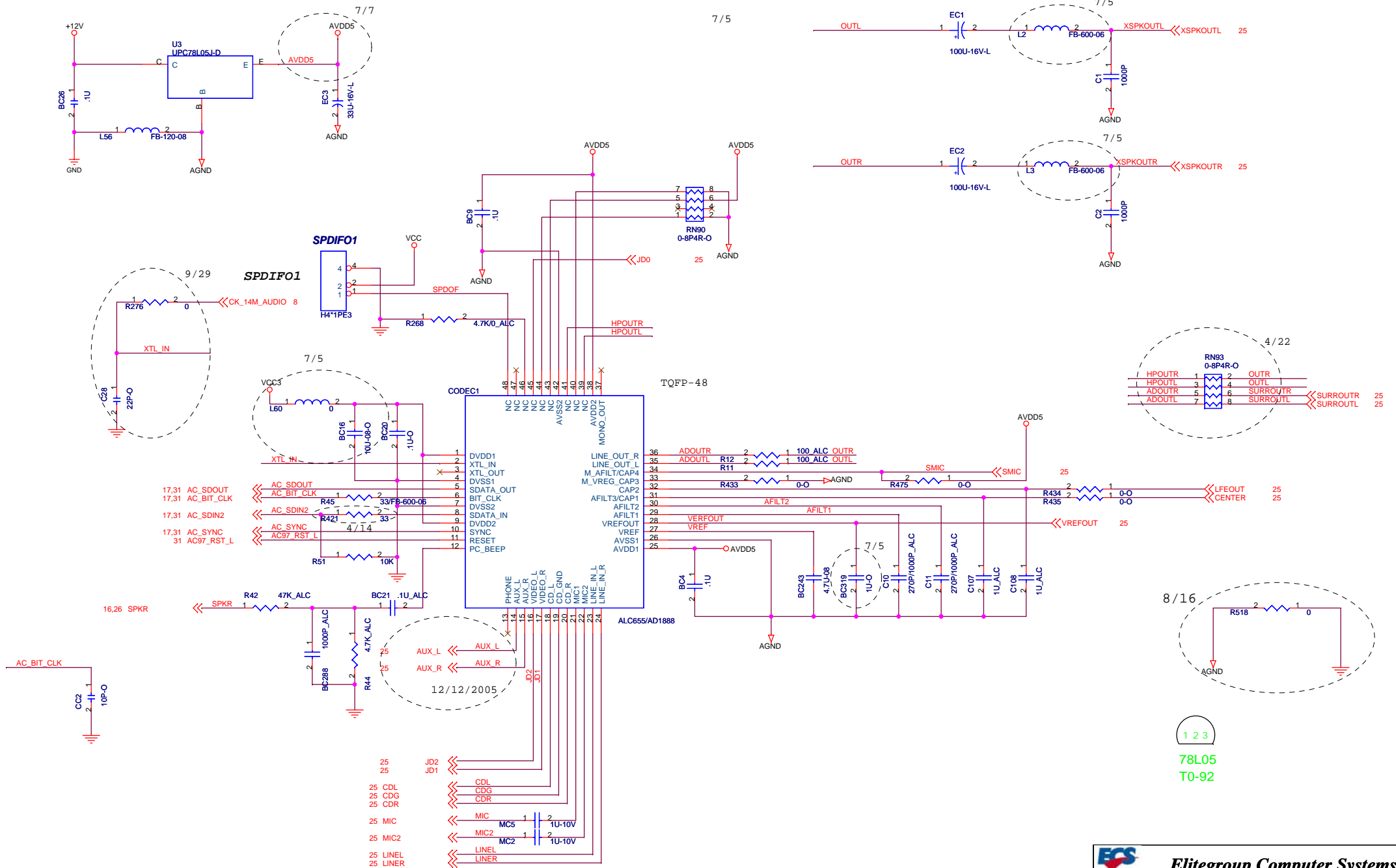


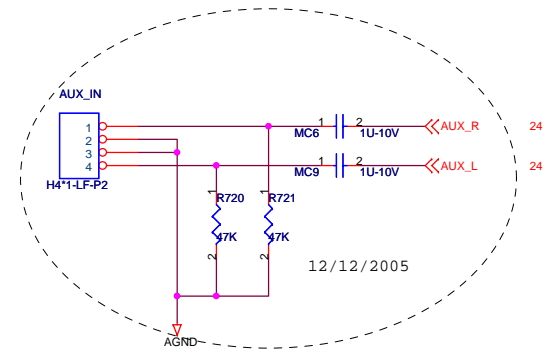
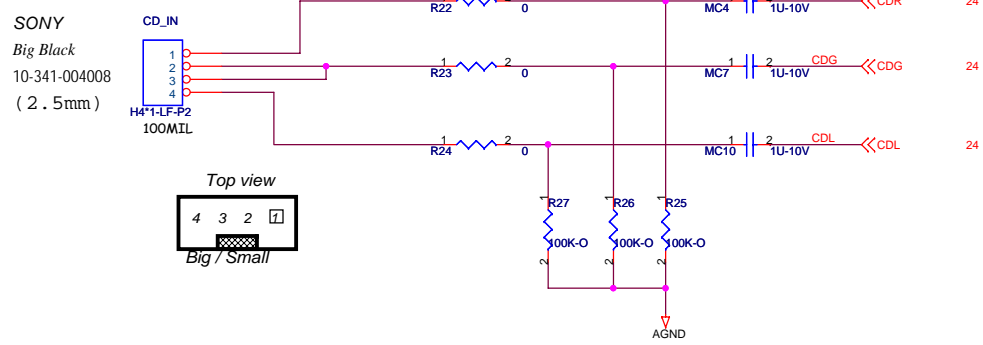
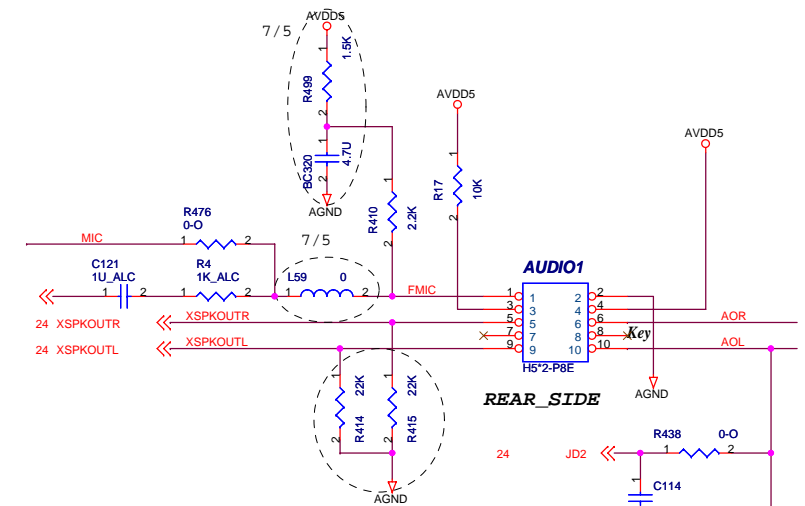
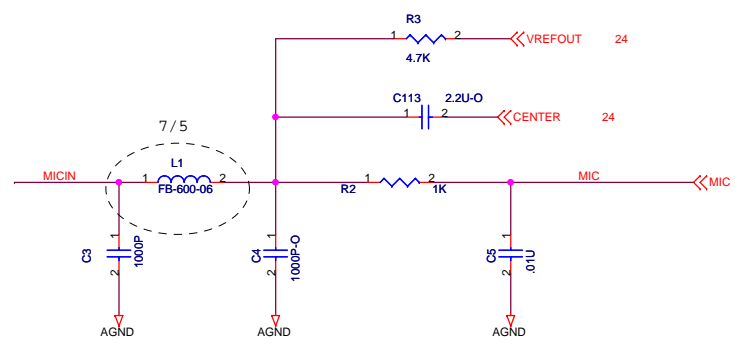
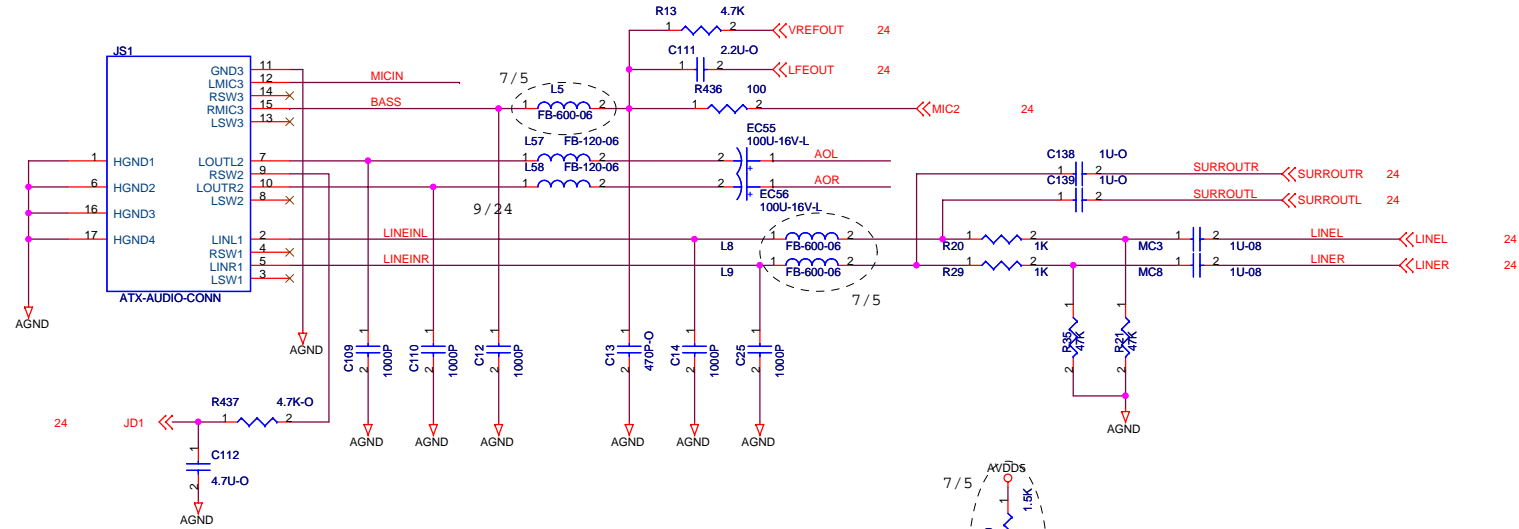
22 NRIAX

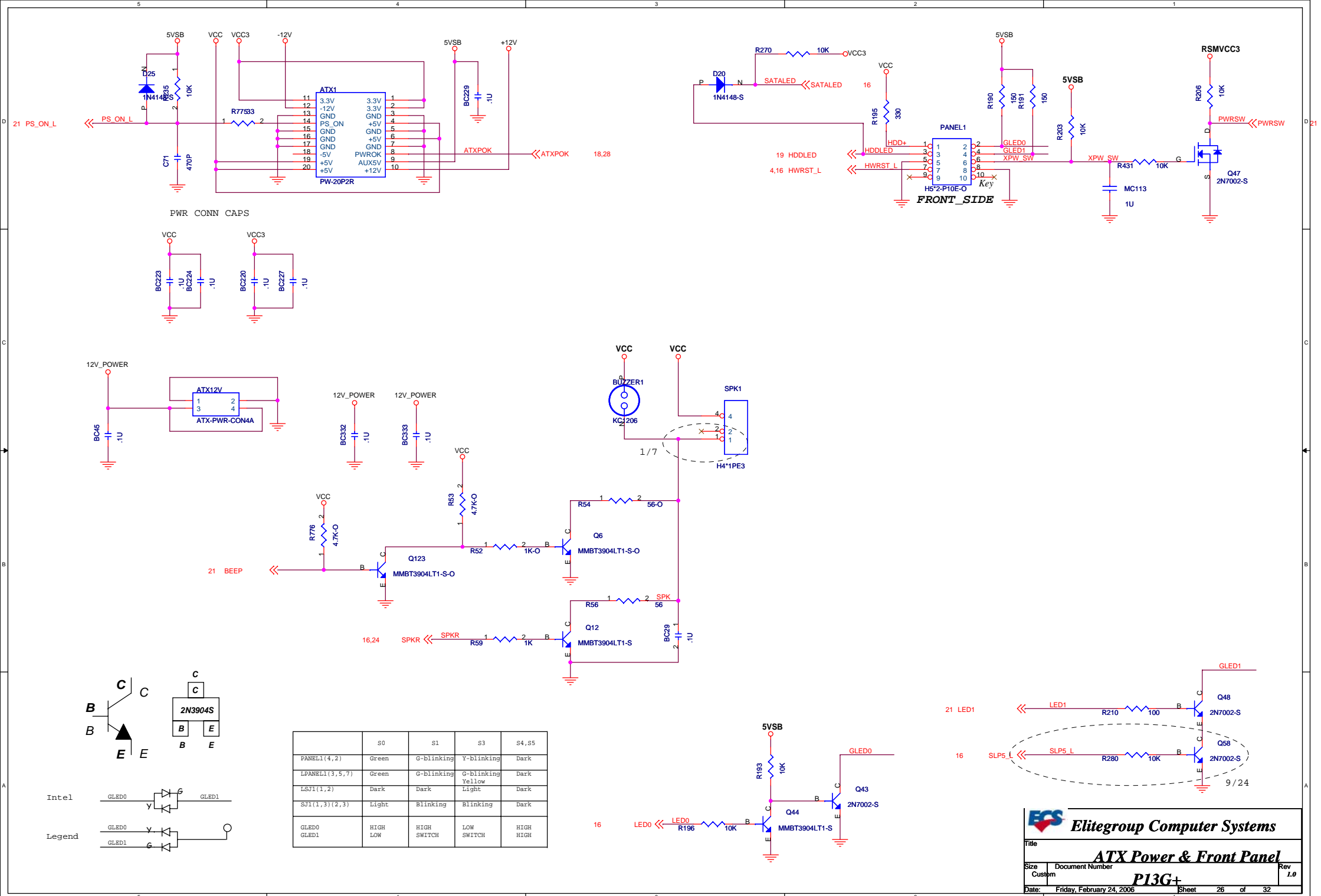


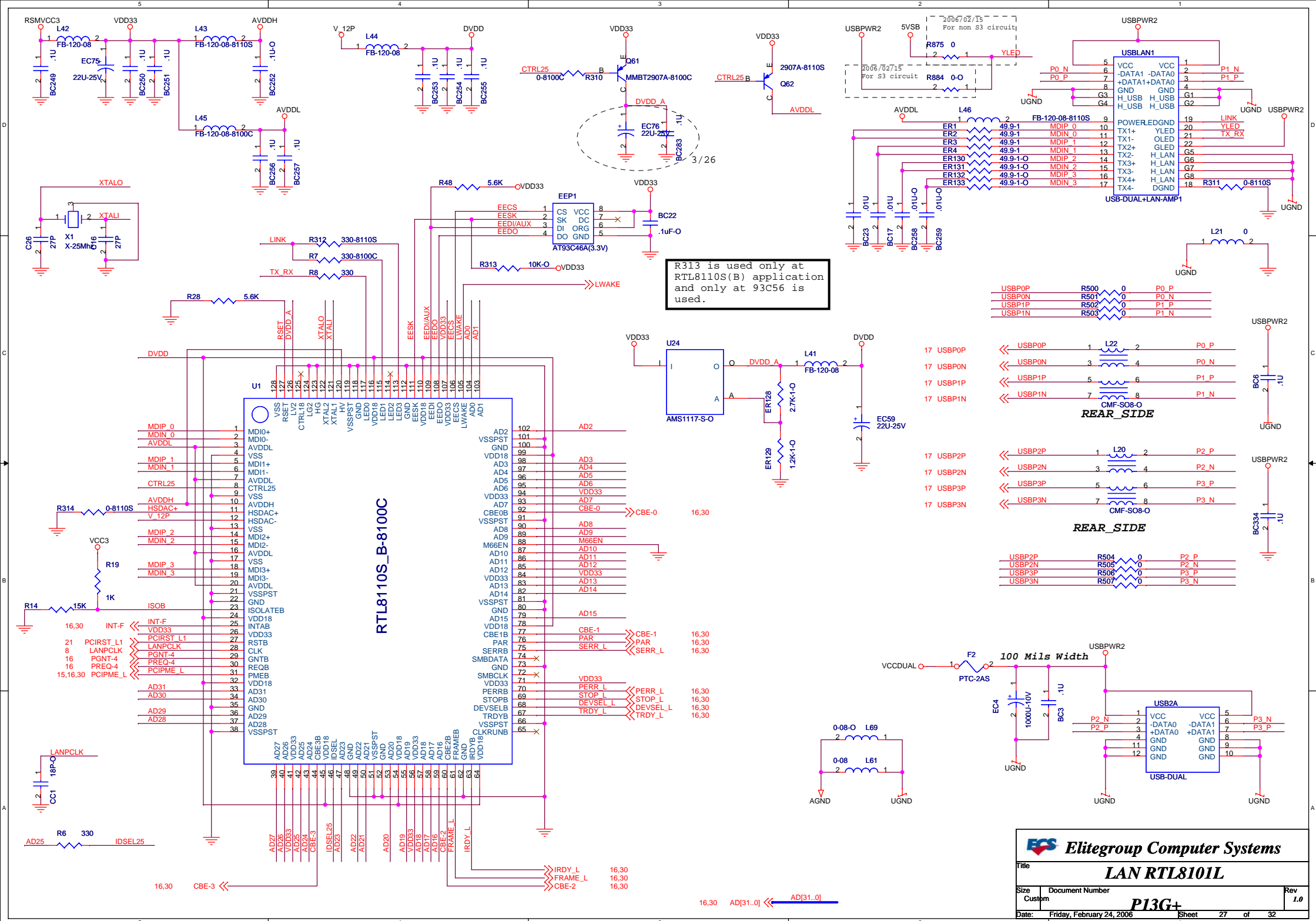
THERM. SENSING

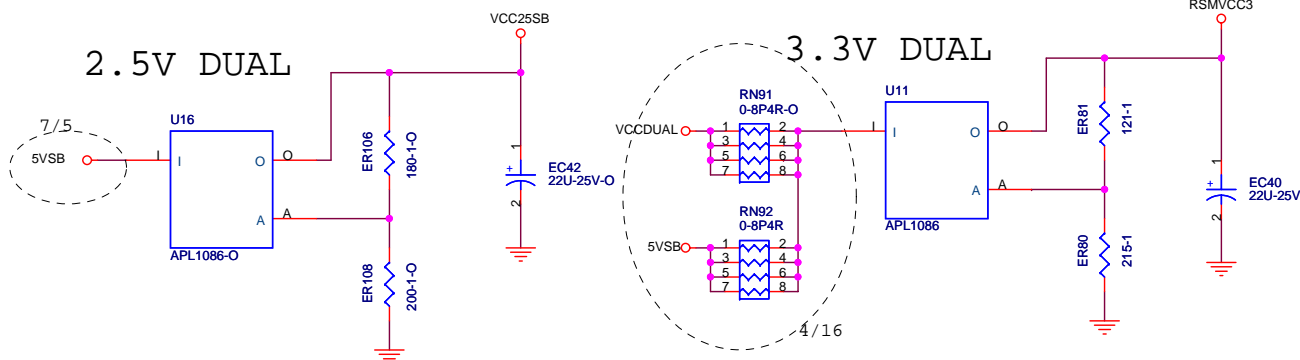
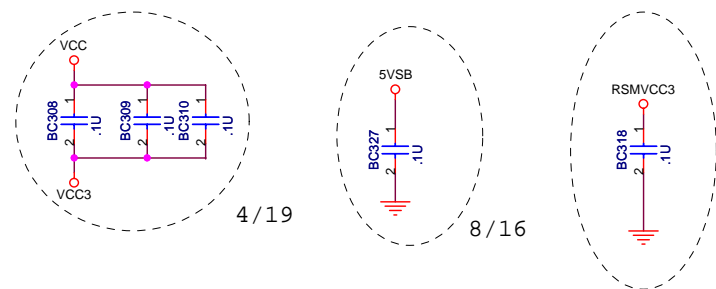


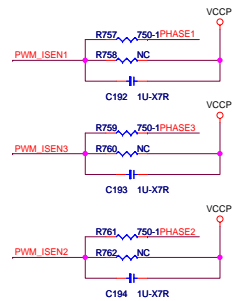
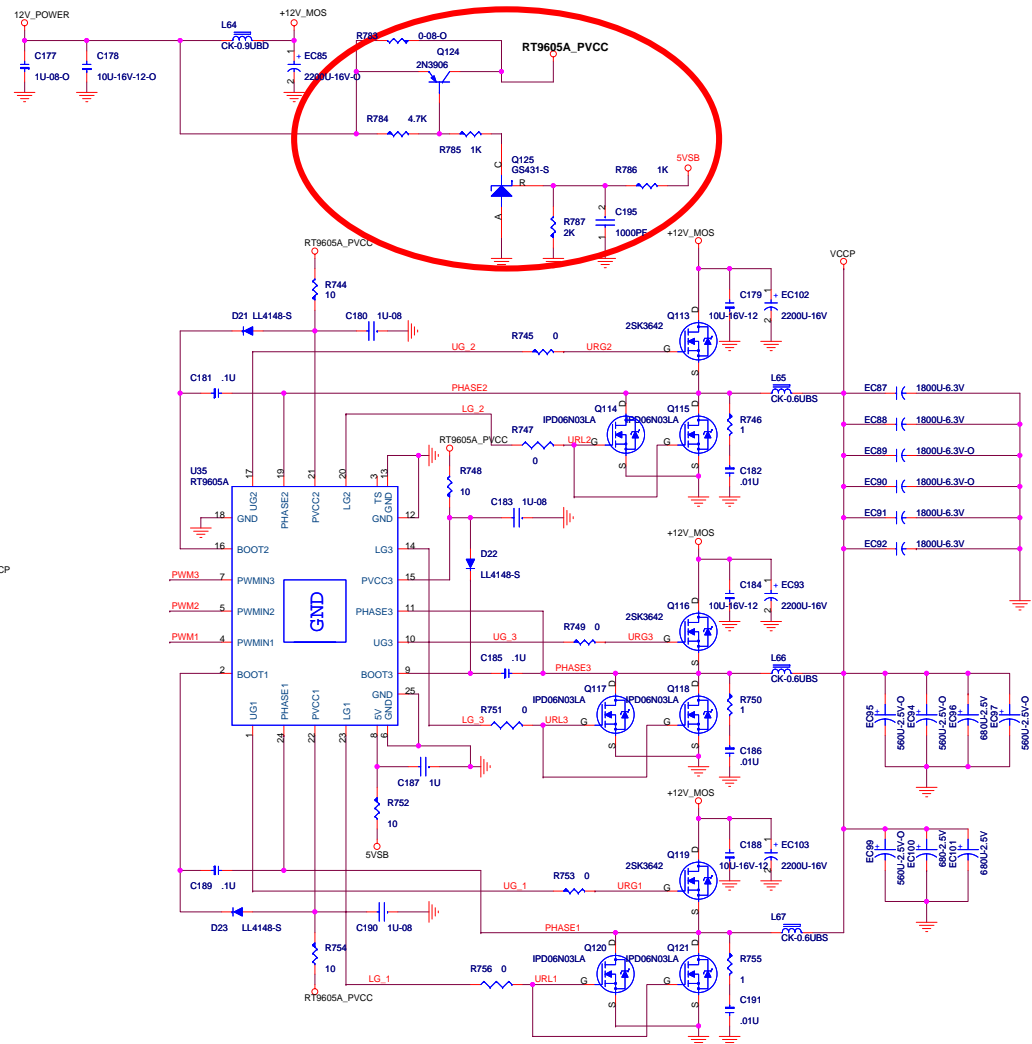


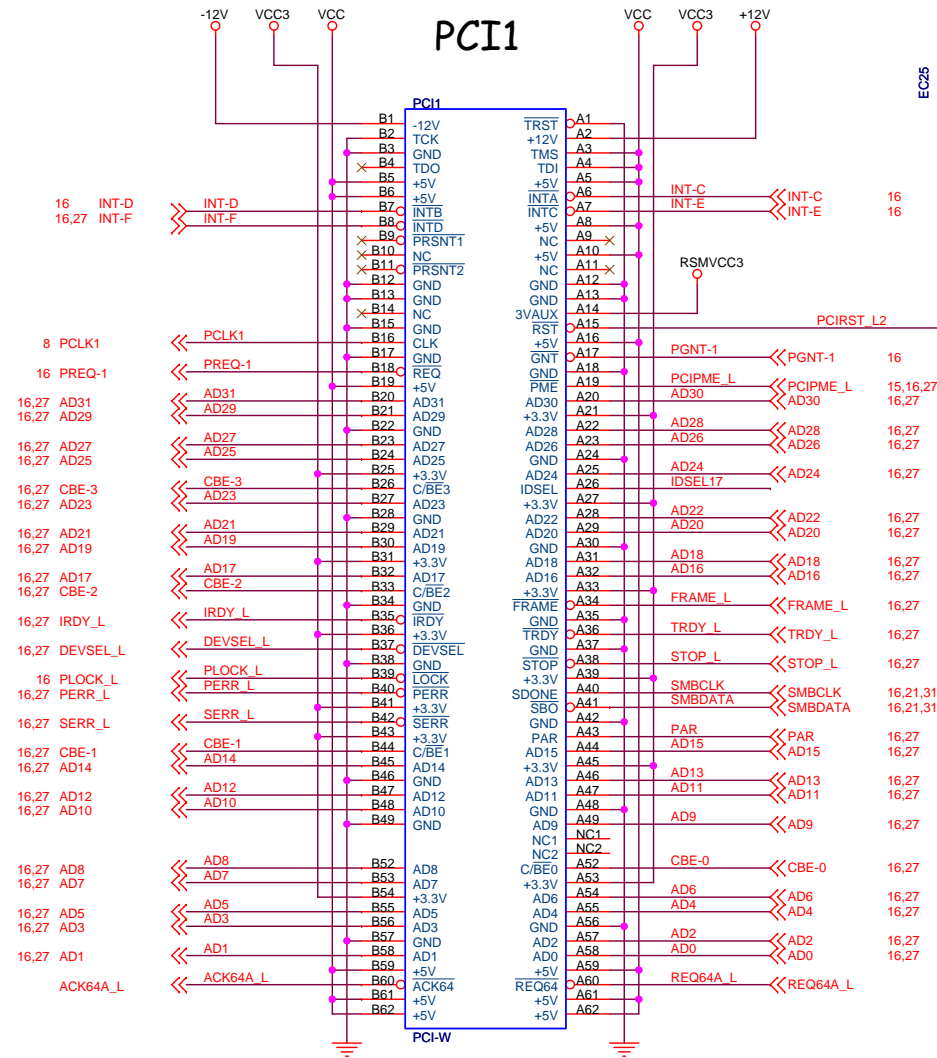










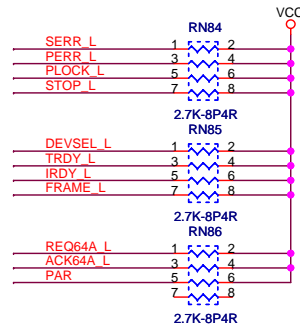
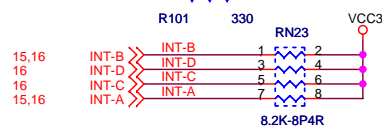


PCI1-INT:
 INTA: INT-C
 INTB: INT-D
 INTC: INT-E
 INTD: INT-F

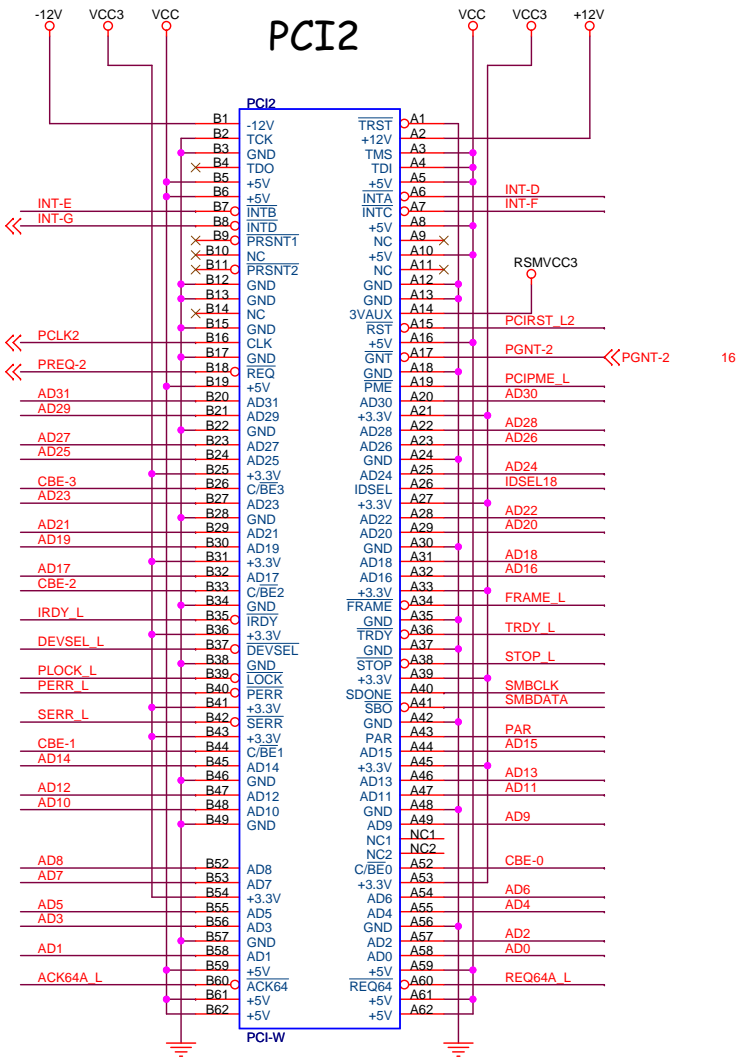
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REQ=PREQ1#

GNT=PGNT1#



16,27 AD[31..0]

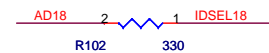


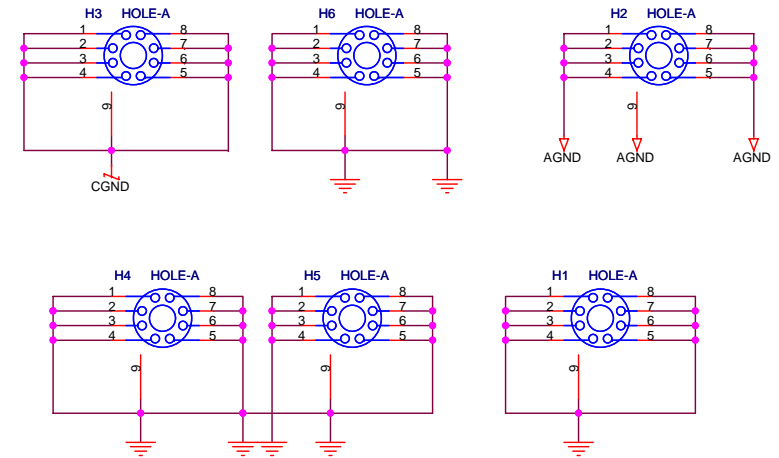
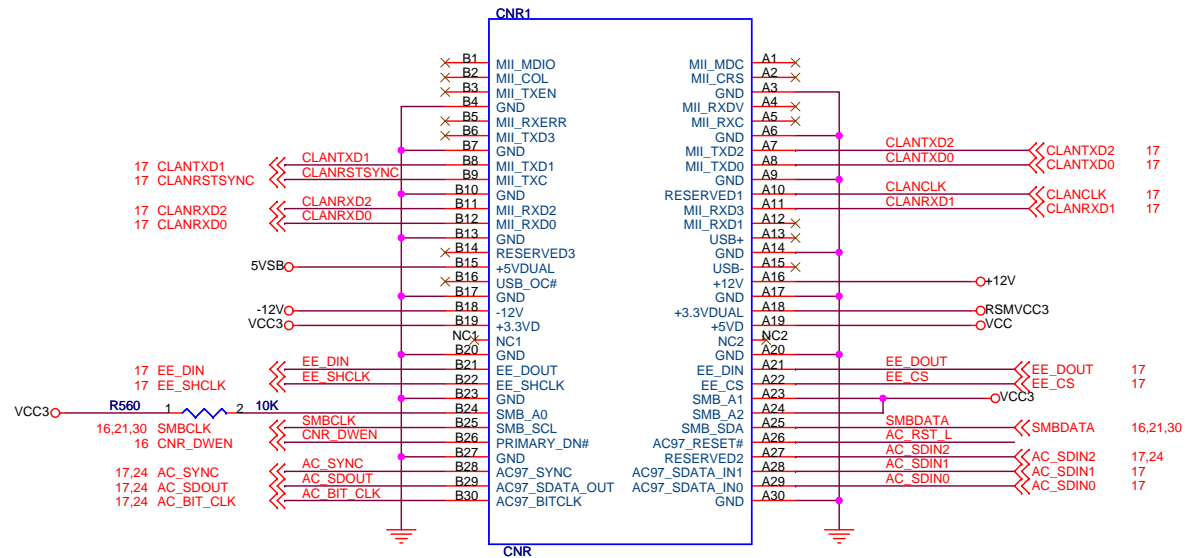
PCI2-INT:
 INTA: INT-D
 INTB: INT-E
 INTC: INT-F
 INTD: INT-G

IDSEL=AD18

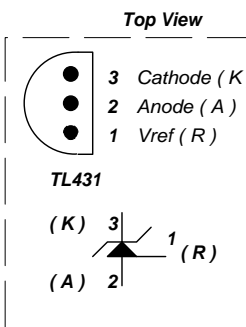
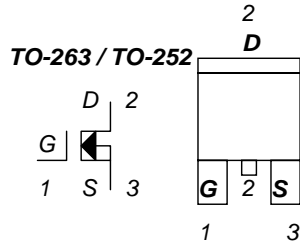
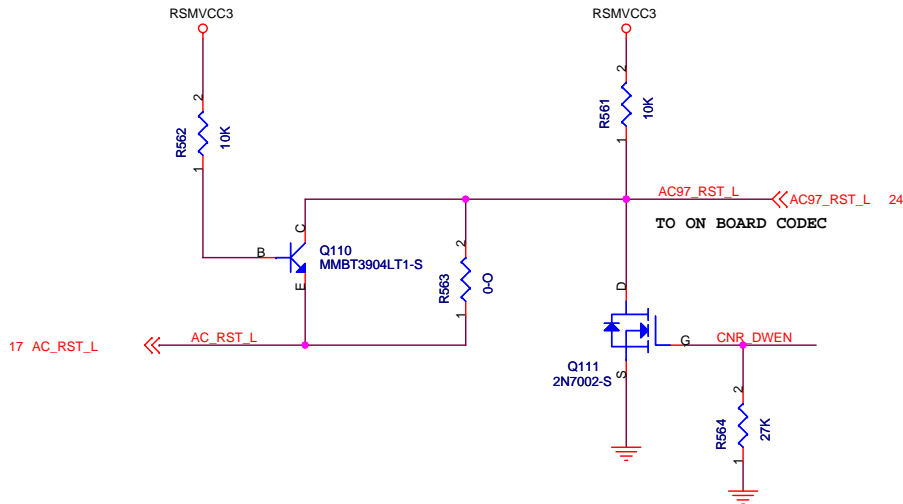
REQ=PREQ2#

GNT=PGNT2#





Top View



SOT-23

